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High speed signaling : jitter modeling, analysis, and budgeting edited by Kyung Suk (Dan) Oh, Xingchao (Chuck) Yuan. Boston, MA : Pearson Education, ©2012.

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High speed signaling : jitter modeling, analysis, and ...

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High-Speed Signaling: Jitter Modeling, Analysis, and Budgeting, Prentice Hall, 2012 • Mike Peng Li, Jitter, Noise and Signal Integrity at High-Speed, Prentice Hall, 2008. ECE 546 -Jose Schutt-Aine 3 • ...

ECE 546 Lecture 23 Jitter Basics - Illinois

Depending on your setup, your jitter problem could be relatively easy to figure out and fix. Check your router's abilities and consider purchasing an upgraded model, connect to it using an Ethernet cable, and make sure you have a reliable, preferably cable-based high-speed internet.

What Is Jitter and How Can I Fix It? - Haste

A synergistic approach to signal integrity for high-speed digital design <p>This book is designed to provide contemporary readers with an understanding of the emerging high-speed signal integrity issues that are creating roadblocks in digital design. Written by the foremost experts on the subject, it leverages concepts and techniques from non-related fields such as applied physics and ...

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They summarize emerging issues and new modeling/analysis methodologies used by leading companies such as Rambus, Intel, and IBM; and thoroughly cover high-speed signaling analysis, including signal and power integrity with on-chip device jitter.

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